

ABSTRACT OF THE DISCLOSURE

- 5 Each of a plurality of ROM correction units in a computer system includes a register for storing a subject address of an original instruction group having a bug, a comparator for comparing a current address against the subject address, a selector for selecting the current address or the branch address of a modified instruction group based on
- 10 the result of the comparison by the comparator, and a flag generator for setting a correction flag when the selector selects the branch address. The computer system recognizes the order of the plurality of ROM correction units based on the correction flag. A plurality of bugs in a program can be modified by respective ROM correction units.